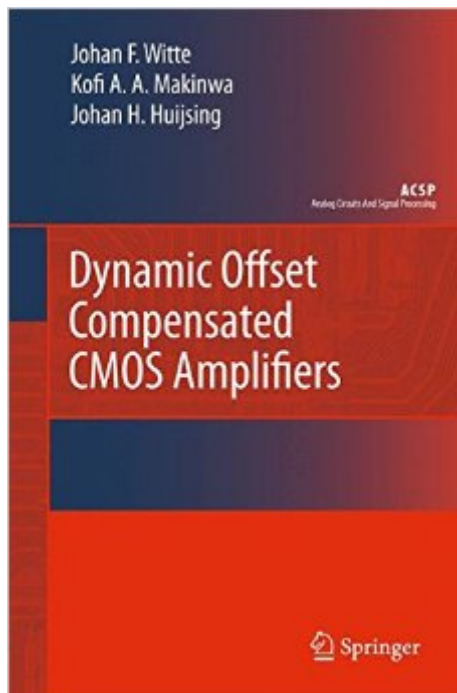


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Dynamic Offset Compensated CMOS Amplifiers (Analog Circuits And Signal Processing)



Synopsis

Dynamic Offset-Compensated CMOS Amplifiers describes the theory, design and realization of dynamic offset compensated CMOS amplifiers. It focuses on the design of general-purpose wide-band operational amplifiers and instrumentation amplifiers. Two offset compensation techniques are described: auto-zeroing and chopping. Several topologies are discussed, with which these techniques can be used in the design of wide-band dynamic offset-compensated amplifiers. Four implementations are discussed in detail: two low-offset wide-band operational amplifiers, a low-offset instrumentation amplifier, and a low-offset current-sense amplifier, which can sense the current drawn from supply voltages up to 28V .

Book Information

Series: Analog Circuits and Signal Processing

Hardcover: 168 pages

Publisher: Springer; 2009 edition (July 21, 2009)

Language: English

ISBN-10: 9048127556

ISBN-13: 978-9048127559

Product Dimensions: 6.1 x 0.4 x 9.2 inches

Shipping Weight: 1.1 pounds (View shipping rates and policies)

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